

Design of high performances Gilbert-cell mixers for GSM/DCS front-ends

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This paper reports an original design methodology for low voltage high performances Gilbert mixers in the DCS range. We present for the first time a real BiCMOS structure featuring +10.6 dB conversion gain, +6.7 dBm TP3, 10.3 DSB noise figure under a 2.2 V supply voltage, which is among the best results ever reported.

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